



# **Surface Mount Schottky Rectifier**





#### **Features**

- Low profile package
- Ideal for automated placement
- Guardring for overvoltage protection
- Low power losses, high efficiency
- High forward surge capability
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C

#### **Typical Applications**

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

#### **Mechanical Date**

• Package: SOD-323FL

Molding compound meets UL 94 V-0 flammability rating, RoHS-compliant, halogen-free

 Terminals: Tin plated leads, solderable per J-STD-002 and JESD22-B102

• Polarity: Cathode line denotes the cathode end

## ■Maximum Ratings (Ta=25 °C Unless otherwise specified)

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PARAMETER	SYMBOL	UNIT	FM16	FM110	FM115	FM120
Device marking code			FM16	FM110	FM115	FM120
Repetitive peak reverse voltage	$V_{RRM}$	٧	60	100	150	200
Average rectified output current @60Hz sine wave, Resistance load, TC (FIG.1)	I <sub>O</sub>	Α	1.0			
Surge(non-repetitive)forward current @ 60Hz half-sine wave,1 cycle, Tj=25℃	l=	А	25			
Forward Surge Current (Non-repetitive) @1ms, square wave, 1 cycle, Tj=25℃	I <sub>FSM</sub>		50			
Current squared time @1ms≲t≲8.3ms Tj =25℃,Rating of per diode	I <sup>2</sup> t	A <sup>2</sup> S	2.6			
Typical junction capacitance @4V,1MHz	Cj	pF	40			
Storage temperature	T <sub>stg</sub>	°C		-55 ~	+175	
Junction temperature	Tj	°C	-55 ~+150 -55 ~+175			

## **■Electrical Characteristics** (Ta=25°C Unless otherwise specified)

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PARAMETER	SYMBOL	UNIT	TEST CONDITIONS	FM16	FM110	FM115	FM120
Peak Forward Voltage	V <sub>F</sub>	٧	I <sub>FM</sub> =1.0A	0.70	0.85	0.	90
Maximum DC reverse current	ted DC blocking voltage I <sub>RRM</sub> mA		T <sub>j</sub> =25°C	0.20			
per diode @ VRM=VRRM			T <sub>j</sub> =125°C	30			

Note1:Pulse test:300uS pulse widh,1% duty cycle

Note2:Pulse test:pulse widh 40mS

# **FM16 THRU FM120**

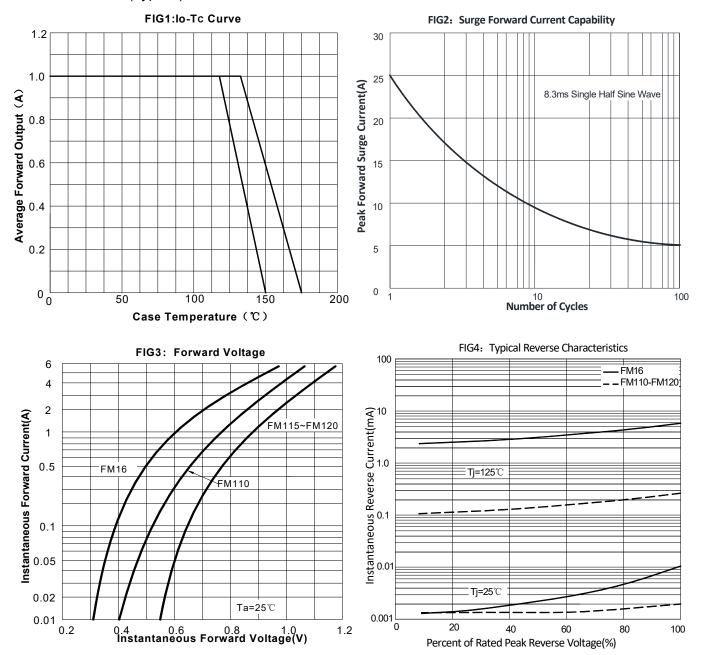
### **■Thermal Characteristics** (Ta=25 °C Unless otherwise specified)

PARAMETER	SYMBOL	UNIT	FM16	FM110	FM115	FM120	
Thermal Desistance	R <sub>θJ-A</sub>	°C 0.01	90 <sup>1)</sup>				
Thermal Resistance R <sub>θJ-C</sub>		°C/W	46 <sup>1)</sup>				

#### Note:

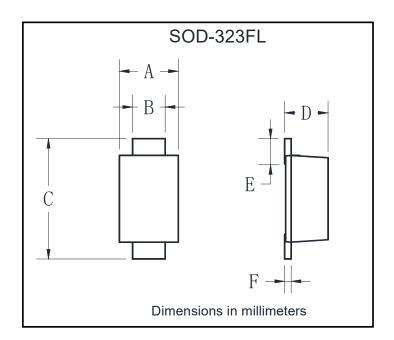
(1) Thermal resistance between junction and ambient and between junction and lead mounted on P.C.B with 3mm\*3mm copper pad areas.

# ■ Characteristics (Typical)



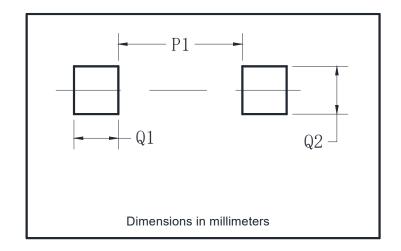


### **■** Outline Dimensions



SOD-323FL				
Dim	Min	Max		
Α	1.05	1.45		
В	0.90	1.15		
С	2.30	2.70		
D	0.80	1.20		
E	0.25	0.70		
F	0.05	0.25		

# ■ Suggested pad layout



SOD-323FL		
Dim	Millimeters	
P1	1.30	
Q1	1.00	
Q2	1.50	



## **FM16 THRU FM120**

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